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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a CON of 09/412,328 10/05/1999 PAT 6,724,065

*yes, pc*

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 10-282335 10/05/1998

*yes, pc*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged	JAPAN	5	1	1
<i>Carmon</i> Examiner's Signature	<i>pc</i> Initials			

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## TITLE

Static random access memory and semiconductor device using MOS transistors having channel region  
 electrically connected with gate

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